

## INFORMATION DISCLOSURE STATEMENT

Applicant : Granneman et al. 10 603 515  
App. No. : Unknown  
Filed : June 24, 2003  
For : METHOD AND APPARATUS FOR  
SUPPORTING A SEMICONDUCTOR  
WAFER DURING PROCESSING  
Examiner : Unknown  
Group Art Unit : Unknown

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing 1 references that are also enclosed.

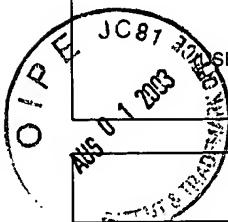
This Information Disclosure Statement is being filed within three months of the filing date of this application and no fee is required in accordance with 37 C.F.R. § 1.97(b)(1), (b)(2), or (b)(4).

Respectfully submitted,  
KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: July 29, 2003

By: Adeel S. Akhtar  
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FORM PTO-1449  INFORMATION DISCLOSURE STATEMENT BY APPLICANT  JC 81 2003 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. ASMINT.002C3	APPLICATION NO. Unknown  10 603 515
REUSE SEVERAL SHEETS IF NECESSARY)	APPLICANT Granneman et al.	
	FILING DATE June 24, 2003	GROUP Unknown



## U.S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

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EXAMINER	DATE CONSIDERED
*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.	